

**NOTES**

- THERE ARE VARIOUS OPTIONS OF MEMORY DEVICE TYPE. EXPECTED ONES ARE SHOWN WITH THEIR LINK CONFIGURATIONS IN THE ADJACENT TABLE.
- THE MEMORY IC PIN NUMBERS IN BRACKETS ARE FOR 28 PIN DEVICES.
- WHEN FITTING 28 PIN DEVICES INTO 32 PIN SOCKETS THE BOTTOM OF THE DEVICE MUST ALIGN WITH THE BOTTOM OF THE SOCKET.
- LK8 SHOULD ONLY BE FITTED IN POSITION 1-2 IF AN ENLARGED MEMORY SPACE WAS REQUIRED. EG. 256KBYTE EPROM IN U2. (NOW DEFAULT POSITION)
- 6264 HAS CS+ ON PIN 26 (A13) SO ONLY WORKS IN EVERY SECOND BK BLOCK.
- IN THE 2-3 POSITION, THE TOP PART OF U2 CAN BE WRITTEN TO WHEN THE DATABASE WRITE ENABLE LINK LK17 IS WRITE ENABLED.

**MEMORY OPTIONS IN U2 EPROM/FLASH**

DEVICE	PINS	LK13	LK22
EPROM			
27C012	28	1-2	1-2
27C010	32	1-2	1-2
27C020	32	2-3	1-2
FLASH			
'010	32	1-2	1-2
'020	32	2-3	1-2
'040	32	2-3	2-3

**MEMORY OPTIONS IN U3 EPROM/FLASH**

DEVICE	PINS	LK1	LK4	LK5	LK23
EPROM					
28C64	28	NP	1-2	NP	1-2
28C256	28	1-2	1-2	NP	1-2
28C010	32	2-3	2-3	2-3	1-2
FLASH					
'010	32	2-3	2-3	2-3	1-2
'020	32	2-3	2-3	2-3	2-3
'040 - AS '020. ONLY HALP VISIBLE.					

**FLASH CHIPS U2/U3**

'010 = 29C010, 29P010, 39SF010
'020 = 29C020, 29P020, 39SF020
'040 = 29C040, 29P040, 39SF040

**RAM ONLY IN U4**

DEVICE	PINS	LK21	LK24
6264	28 (SEE NOTE 5)	1-2	1-2
62256	28	1-2	1-2
628128	32	1-2	1-2
628512	32	2-3	2-3

NF = NOT FITTED

UNLESS OTHERWISE STATED:  
ALL RESISTANCE IN OHMS  
ALL RESISTORS 1/4W, 5%  
ALL CAPACITANCE IN FARADS

PA0797  
PA0798  
PA0799  
PA0804  
PA0990

1-May-2003  
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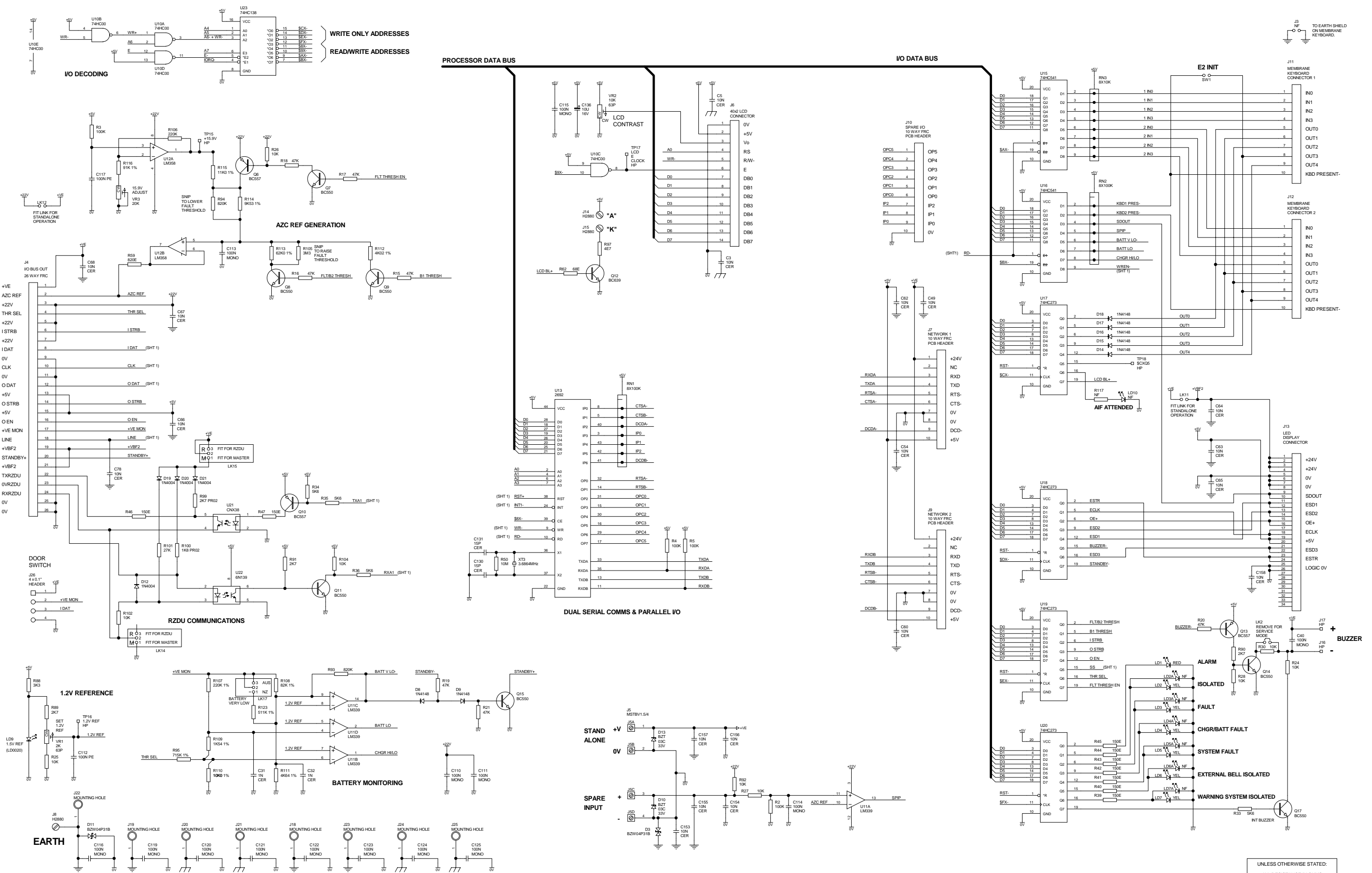
ISS/REV	AMENDMENTS	ECO	DRN	CHKD	AUTH	APVD	DATE
4	CHANGES TO SHEET 2 ONLY.	2986	GEL	KJS	TWH	TWH	24.9.99
4.1	LINK POSITION NUMBERING ADDED TO LK1, 4, 8, 13.	---	---	---	---	---	16.5.00
5	UB WAS TYPE 74HC139. MEMORY SELECTOR LINKS & OPTION TABLES REVISED.	3229	GEL	ER	LSC	DSCP	22.11.01
6	LK1 & 6 DELETED. LK20-24 ADDED. J27 & R119 120, 124, 125, D22 & D23 ADDED. LK16 & R118 ADDED.	3340	GEL	LSC	LSC	DSCP	01.10.02
6	R65 WAS 100K. C99 WAS 100N. PE D24 ADDED.	3349	GEL	LSC	LSC	DSCP	28.02.03
7	TOUTA18 NET CORRECTED TO WAS TOUTA18. R12 WAS 47K.	3349	GEL	LSC	LSC	DSCP	28.02.03
8	LK1 FUNCTION CLARIFIED. NOTE 6 ADDED. DFM CHANGES IMPLEMENTED. LK3 & 10 WERE WIRE	3375	GEL	LSC	LSC	DSCP	24.04.03

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Safety Products  
211 MACES RD, TEL: +64 3 389-5096  
P.O. BOX 19-545, FAX: +64 3 389-5938  
CHRISTCHURCH 8006, NEW ZEALAND

**F3200 CONTROLLER BOARD CIRCUIT DIAGRAM**

DRAWING No: **1931-84** SHEET **1** of **2**

<b>A1</b>	ISS/REV <b>8</b>	PART No: <b>SEE ABOVE</b>
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UNLESS OTHERWISE STATED:  
 ALL RESISTANCE IN OHMS  
 ALL RESISTORS 1/4W, 5%  
 ALL CAPACITANCE IN FARADS

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ISS/REV	AMENDMENTS	ECO	DRN	CHKD	AUTH	APVD	DATE
3	CHANGES TO SHEET 1 ONLY.	2914	GEL	LM	TWH	DSCP	4.2.99
4	C112 & C117 WERE MONO.	2886	GEL	KJS	TWH	TWH	24.9.99
4.1	LINK POSITION NUMBERING ADDED TO LK14.	---	LM	KJS	TWH	DSCP	16.5.00
5	ADDED LK15, LK17, D18-21, R123. DEL. R103, R102 WAS OK. R101 WAS 270E.	3229	GEL	LSC	LSC	DSCP	12.11.01
6	CHANGES TO SHEET 1 ONLY.	3349	GEL	LSC	LSC	DSCP	01.10.02
7	CHANGES TO SHEET 1 ONLY.	3368	GEL	LSC	LSC	DSCP	28.02.03
8	DFM CHANGES IMPLEMENTED.	3375	GEL	LSC	LSC	DSCP	24.04.03

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**F3200 CONTROLLER BOARD CIRCUIT DIAGRAM**

DRAWING No: **1931-84** SHEET **2** of **2**

**A1** ISS/REV **8** PART No: **SEE SHT 1**