



NOTES

- THERE ARE VARIOUS OPTIONS OF MEMORY DEVICE TYPE. EXPECTED ONES ARE SHOWN WITH THEIR LINK CONFIGURATIONS IN THE ADJACENT TABLE.
- THE MEMORY IC PIN NUMBERS IN BRACKETS ARE FOR 28 PIN DEVICES.
- WHEN FITTING 28 PIN DEVICES INTO 32 PIN SOCKETS THE BOTTOM OF THE DEVICE MUST ALIGN WITH THE BOTTOM OF THE SOCKET.
- LK8 WOULD ONLY BE FITTED IN POSITION 1-2 IF AN ENLARGED MEMORY SPACE WAS REQUIRED. EG. 256KBYTE IN U2.
- 6264 HAS CS+ ON PIN 26 (A13) SO ONLY WORKS IN EVERY SECOND 8K BLOCK.

MEMORY OPTIONS IN U2 EPROM/EEPROM/FLASH

DEVICE	PINS	LK3	LK13
EPROM			
TEXAS	27C512 28	NF	1-2
	27C010 32	NF	1-2
	27C020 32	NF	2-3
EEPROM			
SEEQ	28C010 32	1-2	1-2
FLASH			
ATMEL	29C010 32	1-2	1-2

MEMORY OPTIONS IN U3 EPROM/EEPROM/RAM/FLASH

LK6	DEVICE	PINS	LK1	LK4	LK5
EPROM					
TEXAS	27C512 28	2-3	2-3	NF	NF
	27C010 32	2-3	2-3	NF	NF
EEPROM					
SEEQ	28C64 28	NF	1-2	NF	
2-3	28C256 28	1-2	1-2	NF	
2-3	28C010 32	2-3	2-3	1-2	
2-3					
ATMEL	28PC64 28	NF	1-2	NF	
2-3	28C256 28	1-2	1-2	NF	
HITACHI	58C65 28	NF	1-2	NF	
2-3	58C256 28	1-2	1-2	NF	
2-3					
RAM					
HITACHI	6264 28	NF	1-2	NF	
1-2	62256 28	1-2	1-2	NF	
1-2	628128 32	1-2	1-2	2-3	

RAM ONLY IN U4

DEVICE	PINS
HITACHI	6264 28 (SEE NOTE 5)
	62256 28
	628128 32

AR = AS REQUIRED
NF = NOT FITTED

Netlist File: ElectricalRulesCheck
ELECTRICAL RULES CHECK
C:\ADVFILES\1931-84B.ERC

Netlist CreateNetList
GENERATE NETLIST
C:\ADVFILES\1931-84B.NET

1931-84B 502
Sheet 2

**PA0797
PART NUMBER
PA0799**

5-Nov-1998
12:40:50

F3200 CONTROLLER VERSIONS

VERSION	PA No	DESCRIPTION	RZDU COMM5	DUART & SPARE I/O	RAM	EEPROM
1931-84-1	PA0797	FIP CONTROLLER / DISPLAY	MASTER	U13-NF	32K	8K
1931-84-2	PA0798	RZDU CONTROLLER / DISPLAY	RZDU	U13-NF	32K	32K
1931-84-3	PA0799	PROTOCOL TRANSLATION MOD.	MASTER	FITTED	128K	32K

UNLESS OTHERWISE STATED: ALL DIMENSIONS IN MILLIMETRES. DO NOT SCALE. TOLERANCES ARE TO BE: 1 DECIMAL PLACE +/-0.5, 2 DECIMAL PLACES +/-0.3, 3 DECIMAL PLACES +/-0.1

CONFIDENTIAL: THIS DOCUMENT MUST NOT BE COPIED NOR THE CONTENTS PASSED ON TO ANY THIRD PARTY WITHOUT THE CONSENT OF THE COMPANY.

ISS/REV	AMENDMENTS	ECO	DRN	CHKD	AUTH	APVD	DATE
1	PCB RELAS - FIRST PROTOTYPE (ORIGINAL WAS 1931-2)		TJH	NH	RAC	DSCP	7.8.97
2	RELEASED FOR PRODUCTION		TJH	NH			29.9.97

F3200 CONTROLLER BOARD CIRCUIIT DIAGRAM

VIGILANT
FIRE & EVACUATION SYSTEMS

211 MACES ROAD, CHRISTCHURCH 8006, NZ
P.O. BOX 19-545, CHRISTCHURCH 8030, NZ

SIZE	DRAWING NUMBER	SHEET NUMBER	ISS/REV	PART NUMBER
A1	1931-84	1 of 2	2	As Above