



NOTES

1. THERE ARE VARIOUS OPTIONS OF MEMORY DEVICE TYPE. EXPECTED ONES ARE SHOWN WITH THEIR LINK CONFIGURATIONS IN THE ADJACENT TABLE.
2. THE MEMORY IC PIN NUMBERS IN BRACKETS ARE FOR 28 PIN DEVICES.
3. WHEN FITTING 28 PIN DEVICES INTO 32 PIN SOCKETS THE BOTTOM OF THE DEVICE MUST ALIGN WITH THE BOTTOM OF THE SOCKET.
4. LK5 WOULD ONLY BE FITTED IN POSITION 1-2 IF AN ENLARGED MEMORY SPACE WAS REQUIRED. EG. 256KBYTE EPROM IN U2.
5. 6264 HAS CS+ ON PIN 26 (A13) SO ONLY WORKS IN EVERY SECOND BK BLOCK.

MEMORY OPTIONS IN U2 EPROM/EEPROM/FLASH

DEVICE	PINS	LK3	LK13
EPROM			
TEXAS	27C512 28	NF	1-2
	27C010 32	NF	1-2
	27C020 32	NF	2-3
EEPROM			
SEEQ	28C010 32	1-2	1-2
FLASH			
ATMEL	29C010 32	1-2	1-2

MEMORY OPTIONS IN U3 EPROM/EEPROM/RAM/FLASH

DEVICE	PINS	LK1	LK4	LK5
EPROM				
TEXAS	27C512 28	2-3	2-3	NF
	27C010 32	2-3	2-3	NF
EEPROM				
SEEQ	28C64 28	NF	1-2	NF
	28C256 28	1-2	1-2	NF
	28C010 32	2-3	2-3	1-2
	28C010 32	2-3	2-3	1-2
ATMEL	28P064 28	NF	1-2	NF
	28C256 28	1-2	1-2	NF
	28C256 28	1-2	1-2	NF
HITACHI	58C05 28	NF	1-2	NF
	58C256 28	1-2	1-2	NF
	58C256 28	1-2	1-2	NF
RAM				
HITACHI	6264 28	NF	1-2	NF
	62256 28	1-2	1-2	NF
	628128 32	1-2	1-2	2-3
	628128 32	1-2	1-2	2-3
FLASH				
ATMEL	29C010 32	2-3	2-3	2-3

RAM ONLY IN U4

DEVICE	PINS
HITACHI	6264 28 (SEE NOTE 5)
	62256 28
	628128 32

AR = AS REQUIRED
NF = NOT FITTED

Netlist Run/Electrical Rules Check
ELECTRICAL RULES CHECK
C:\ADVFILES\191-84-ERC

Netlist Create/Netlist Generate Netlist
C:\ADVFILES\191-84-NET

1910944.902
Sheet 2

PA0797
PA0798
PA0799

UNLESS OTHERWISE STATED:
ALL RESISTANCE IN OHMS
ALL RESISTORS 1/4W 5%
ALL CAPACITANCE IN FARADS

29-Jan-2001
11:47:39

F3200 CONTROLLER VERSIONS

VERSION	PA No	DESCRIPTION	RZDU COMMS	DUART & SPARE I/O	RAM	EEPROM
1931-84-1	PA0797	FIP CONTROLLER / DISPLAY	MASTER	U13-NF	32K	8K
1931-84-2	PA0798	RZDU CONTROLLER / DISPLAY	RZDU	U13-NF	32K	32K
1931-84-3	PA0799	PROTOCOL TRANSLATION MOD.	MASTER	FITTED	128K	32K

UNLESS OTHERWISE STATED: ALL DIMENSIONS IN MILLIMETRES. DO NOT SCALE. TOLERANCES ARE TO BE: 1 DECIMAL PLACE +/-0.5, 2 DECIMAL PLACES +/-0.3, 3 DECIMAL PLACES +/-0.1

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ISS/REV	AMENDMENTS	ECO	DRN	CHKD	AUTH	APVD	DATE
1	PCB RELAD - FIRST PROTOTYPE (ORIGINAL WAS 1931-2)			TWH	RAC	DSCP	7.8.97
2	RELEASED FOR PRODUCTION						29.9.07
3	C95 & C99 WERE 100N MONO. R90 WAS 1M			GEL	LM	TWH	DSCP
4	CHANGES TO SHEET 2 ONLY			2886	GEL	RJS	TWH
4.1	LINK POSITION NUMBERING ADDED TO LK1, 4.6, 13				LM		17.5.00

F3200 CONTROLLER BOARD CIRCUIT DIAGRAM

VIGILANT
FIRE & EVACUATION SYSTEMS

211 MACES ROAD, CHRISTCHURCH 8006, NZ
P.O. BOX 19-545, CHRISTCHURCH 8030, NZ

SIZE	DRAWING NUMBER	SHEET NUMBER	ISS/REV	PART NUMBER
A1	1931-84	1 of 2	4.1	As Above